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TRANSMITTAL FORM (to be used for all correspondence after initial filing)	Application Number	10/044,206
	Filing Date	January 11, 2002
	First Named Inventor	Jerome Michael Eldridge
	Group Art Unit	2813
	Examiner Name	Unknown
Total Number of Pages in This Submission	Attorney Docket Number MI22-1914	

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ENCLOSURES (check all that apply)		
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SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT	
Firm or Individual name	D. Brent Kenady Reg. No. 40,045; Wells St. John P.S.
Signature	
Date	3-12-02

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial No. 10/044,206
Filing Date January 11, 2002
Inventor Jerome Michael Eldridge
Assignee Micron Technology, Inc.
Group Art Unit 2813
Examiner Not Yet Assigned
Attorney's Docket No. MI22-1914
Title: Methods of Forming Void Regions, Dielectric Regions and Capacitor Constructions

#5
8-22-02
Payton

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

References -- See Attached Form PTO-1449

TO: Assistant Commissioner for Patents
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REMARKS

The Examiner's attention is directed to the references which are listed on the attached Form PTO-1449 and copies of which are attached. No admission is made regarding whether all the submitted references are prior art.

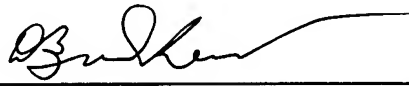
Citation of these patents is respectfully requested.

This Information Disclosure Statement is being filed within three months of the filing date of the application or before the mailing of a first Office Action, whichever occurs last. Therefore, no fee is believed to be required. However, in the event that a fee is required for filing this information disclosure statement, please charge the fee specified under 37

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Respectfully submitted,

Date: 3-12-02

By: 
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